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Ann R. Lanfri  
**21st Proceedings of the Design Automation Conference on Design automation** June 1984  
 Caesar is an interactive graphic editor that is used to generate layouts for VLSI circuits. It runs on a VAX-11/780 under the UNIX 1 operating system with Berkeley extensions. Caesar has a unique and simple user interface. Phled is an enhanced version of Caesar that runs on a M68000-based engineering work station. Phled45 is based on Phled; its distinctive feature is the ability to enter layout containing 45° angles. The design considerations for implementing the ca ...
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Andrew B. Kahng , Gabriel Robins , Anish Singh , Huijuan Wang , Alexander Zelikovsky  
**Proceedings of the 1998 international symposium on Physical design** April 1998  
 In very deep-submicron VLSI, certain manufacturing steps &mdash notably optical exposure, resist development and etch, chemical vapor deposition and chemical-mechanical polishing (CMP)&mdash have varying effects on device and interconnect features depending on local characteristics of the layout. To make these effects uniform and predictable, the layout itself must be made uniform with respect to certain density parameters. Traditionally, only foundries have performed the p ...
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Daniel S. Whelan  
**Proceedings of the 9th annual conference on Computer graphics and interactive techniques** July 1982  
 A display system architecture which has rectangular area filling as its primitive operation is presented. It is shown that lines can be drawn significantly faster with this architecture than with a pixel display system. The rendition of filled boxes is also faster showing an O(n<sup>2</sup>) speed improvement. Furthermore filled polygons can be rendered with an O(n) speed improvement. The design and implementation of th ...
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D. Stark , M. Horowitz  
**24th ACM/IEEE conference proceedings on Design automation conference** October 1987  
 This paper describes an extractor designed to produce resistance values for use in digital circuit simulation. REDS avoids resistance extraction on most nets in a design using a simple filter based on the perimeter and area values calculated by the capacitance extractor, allowing it to concentrate on areas where resistance may substantially affect circuit timing. Nets are extracted using a fast square counting algorithm, and simplified before output to remove spurious elements. REDS is desi ...
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Michael Brady  
**ACM Computing Surveys (CSUR)** January 1982  
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**Proceedings of the 1998 IEEE/ACM international conference on Computer-aided design** November 1998
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James C. Lin

**Wireless Networks** November 1997

Volume 3 Issue 6

Our knowledge on the biological effects of RF radiation has been increasing for many decades. It has become a focus of attention because of the accelerated use of RF radiation for wireless communication over the past few years. It is fairly well established that at sufficiently high power levels, RF and microwave energy can produce deleterious biological effects. Wireless communication systems use low power modulated forms of RF and microwave radiation that was not investigated extensively ...

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Pascal Volino , Martin Courchesne , Nadia Magnenat Thalmann

**Proceedings of the 22nd annual conference on Computer graphics and interactive techniques** September 1995

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